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1.55 μm room-temperature lasing from subwavelength quantum-dot microdisks directly grown on (001) Si

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Miniaturized laser sources can benefit a wide variety of applications ranging from on-chip optical communications and data processing, to biological sensing. There is a tremendous interest in integrating these lasers with rapidly advancing silicon photonics, aiming to provide the combined strength of the optoelectronic integrated circuits and existing large-volume, low-cost silicon-based manufacturing foundries. Using III-V quantum dots as the active medium has been proven to lower power consumption and improve device temperature stability. Here, we demonstrate room-temperature InAs/InAlGaAs quantum-dot subwavelength microdisk lasers epitaxially grown on (001) Si, with a lasing wavelength of 1563 nm, an ultralow-threshold of 2.73 μW, and lasing up to 60 °C under pulsed optical pumping. This result unambiguously offers a promising path towards large-scale integration of cost-effective and energy-efficient silicon-based long-wavelength lasers. Published by AIP Publishing. [http://dx.doi.org/10.1063/1.4979120]

Silicon photonics has gone through immense advancements over the past two decades,1 paving the way towards optoelectronic integrated circuits and inter/intra-chip optical interconnections. Although group-IV-based optical building blocks including silicon modulators,2 microring resonators,3 waveguides,4 and germanium photodetectors5 have demonstrated their techno-economic viability, an efficient Si-based laser source remains the most challenging component due to the indirect bandgap of Si.6 Integrating high performance III-V lasers with Si has been considered a promising path to resolve this challenge. Wafer or die bonding has been shown as a successful approach, demonstrating impressive device performances,7–9 and has gradually been adopted by industry. For large-scale photonic integrations, this method introduces process complexity with some remaining issues.10 Recently, direct epitaxy of III-V lasers on Si has attracted significant attention with the successful use of self-assembled quantum dots (QDs) as active materials.11–13 The distinctive zero-dimensional density of states of QDs offers lower threshold current density with improved thermal stability in III-V lasers,14 and their discrete localization promises greater immunity to defects associated with III-V/Si heteroepitaxy when compared to their conventional quantum-well counterparts.15 More interestingly, these QDs are capable of bending or pinning the threading dislocations (TDs) because of their large strain field.16,17 Recently, high performance electrically pumped continuous-wave InAs/GaAs QD lasers with an emission wavelength as long as 1315 nm have been directly grown on offcut silicon substrates,11 yielding a low threshold current density of 62.5 A cm−2, and an elevated operation temperature up to 120 °C. These lasers have shown respectable operating lifetimes over 3100 h. To exploit the 1550 nm telecom wavelength where long-distance communication can benefit from the least attenuation, developing InP-based QD lasers on silicon is necessary. In this work, optically pumped subwavelength InAs/InAlGaAs quantum dot microdisk lasers (MDLs) epitaxially grown on CMOS-compatible (001) Si emitting at 1.55 μm are reported. These compact microdisk lasers are advantageous for low power consumption and high quality factor (a few thousands).6,18 Shrinkage of the disk size opens new venues for future nanophotonic circuits and dense integration. Using QDs as the active medium to effectively counter balance the surface recombination,19 as well as optimizing the smoothness of the disk sidewall, we are able to overcome the radiative losses and scale the microdisk diameter down to 1.5 μm. With better localized carriers inside the QDs, the internal quantum efficiency of these large surface-to-volume MDLs should be higher than 48%,20 a theoretical estimate for similar sized MDLs with QWs as gain material. Under pulsed pumping, these subwavelength disks exhibit room-temperature thresholds as low as 2.73 μW and lasing operation up to 60 °C (limited by the measurement setup), with a characteristic temperature as high as 123 K. Statistical analysis based on a variety of disks suggests good device performance when compared with the same microdisk structure on native InP substrates.

All the material growth including the InP-on-Si (IoS) buffer and the microdisk laser structure were completed using an AIX-200/4 metalorganic chemical vapor deposition (MOCVD) system. To fabricate InAs QD MDLs on Si, it is necessary to minimize dislocations that introduce non-radiative recombination and deplete carriers for lasing. Direct epitaxy of InP on Si (~8% lattice mismatch) usually leads to a high density of defects including TDs (109–1010/cm2) and anti-phase-boundaries (APBs).21 We have employed several

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strategies to reduce defect densities, as shown in the cross-sectional transmission electron microscopy (TEM) image (Fig. 1(a)); First, a 1.7-μm-thick GaAs intermediate buffer provides an APB-free III-V surface and accommodates the large mismatch between InP and Si. Inside the GaAs, a ten-stack AlGaAs/GaAs strained-layer superlattice (SLS) was used to reduce dislocation density and to smooth the surface. Secondly, a thin LT-InP nucleation layer (70 nm) was carefully optimized to ensure good island coverage, followed by a 1.4 μm InP single crystal buffer layer deposited at multi-step temperatures. A 60 nm In0.6Ga0.4As strained interlayer was inserted to filter the dislocations generated at the interface of InP/GaAs. A typical 5 × 5 μm² atomic force microscopy (AFM) image of the as-grown InP buffer surface (Fig. 1(e)) shows a smooth morphology with root-mean square roughness of 1.6 nm. MDL consisting of a seven-period InAs/InGaAs/InAlGaAs dot-in-well (DWELL) structure sandwiched by InAlAs claddings (Fig. 1(b)) was grown on the APB-free IoS templates. Figure 1(c) presents a close-up TEM image of the MDL epi-structure on silicon. The InAs QDs with a typical diameter of 1.4 μm and InGaAs/InAlGaAs dot-in-well (DWELL) structure sandwiched between InAlAs claddings (Fig. 1(b)) was grown on the APB-free IoS templates. Figure 1(c) presents a close-up TEM image of the MDL epi-structure on silicon. The InAs QDs with a typical diameter of 1.4 μm and InGaAs/InAlGaAs dot-in-well (DWELL) structure sandwiched by InAlAs claddings (Fig. 1(b)) was grown on the APB-free IoS templates.22 A typical 5 × 5 μm² atomic force microscopy (AFM) image of the as-grown InP buffer surface (Fig. 1(e)) shows a smooth morphology with root-mean square roughness of 1.6 nm. MDL consisting of a seven-period InAs/InGaAs/InAlGaAs dot-in-well (DWELL) structure sandwiched by InAlAs claddings (Fig. 1(b)) was grown on the APB-free IoS templates.22 A typical 5 × 5 μm² atomic force microscopy (AFM) image of the as-grown InP buffer surface (Fig. 1(e)) shows a smooth morphology with root-mean square roughness of 1.6 nm. MDL consisting of a seven-period InAs/InGaAs/InAlGaAs dot-in-well (DWELL) structure sandwiched by InAlAs claddings (Fig. 1(b)) was grown on the APB-free IoS templates.23 Figure 1(c) presents a close-up TEM image of the MDL epi-structure on silicon. The InAs QDs with a typical diameter of ~45 nm (Fig. 1(d)) were grown on top of a strained InGaAs wetting layer and immediately capped by a thin LT-InAlGaAs (1.5 nm) to avoid desorption. Then, a thick high-temperature (HT) InAlGaAs spacer (52 nm) was deposited to anneal these truncated QDs, preventing dot height dispersion and tuning the emission wavelength. A representative AFM image of the uncapped InAs QDs on the IoS substrate is illustrated in Fig. 1(d), with a density of ~4 × 10¹⁰ cm⁻². A few large clusters resulting from the bumpy InP underneath were identified. For benchmarking, the same MDL structure was also grown on an InP (001) native substrate in the same growth run. Normalized room-temperature photoluminescence (RT-PL) of both samples centered at ~1500 nm is shown in Fig. 1(f). The peak intensity for the MDL on IoS was 3.5 times lower than the one grown on InP, with a shoulder apparent in the higher energy side, which can be attributed to the dot size fluctuation. The broad spectra however favor the interaction between resonant modes in small cavities and the material gain.

Ultra-small MDLs (1.5 μm in diameter) were fabricated using the colloidal lithography method.25 Dry-etch was performed to define the disk region and was terminated at the InP buffer layer, followed by a selective wet-etch to form the InP pillar. A typical 70° tilted scanning electron microscope (SEM) image in the inset of Fig. 3(c) clearly reveals a smooth and steep sidewall, as well as a cylindrical geometry. The well-developed fabrication process greatly benefits the whispering-gallery mode (WGM) oscillation around the disk periphery, enabling these devices to operate above room temperature.

Figure 2(a) shows the room temperature emission spectra of an as-fabricated MDL. The micro-PL measurements were performed using a 532 nm Q-switched Nd:YAG pulsed laser source (20 ns pulse width, 3000 Hz repetition rate). The average input power reaching the top surface of MDLs was calibrated by a power-meter. At a low excitation power of 2.1 μW, a weak cavity mode started to emerge. Meanwhile, linewidth narrowing phenomenon was observed as the pump power increased below the lasing threshold (Fig. 2(b)). Further increase of the injection power led to stimulated emission at ~1563 nm with a high background suppression ratio of 21.7 dB, as shown in the inset of Fig. 2(a). In addition, the linewidth of the lasing peak enlarged as the pump power was raised above the threshold due to wavelength chirping, which was associated with changes in refractive index induced by transient changes of carrier density in the QDs.26 The cold cavity quality factor is calculated by Q = λ/Δλ ~ 1563. In the inset of Fig. 2(a), no peak wavelength shift can be observed with progressively increased pumping power, suggesting negligible thermal generation inside the disk region and good dissipation through the InP pillar and the IoS substrate. Figure 2(b) demonstrates the integrated output intensity as a function of injection power (L-L curve), with a clear “kink” appearing at the lasing threshold (Pth) of 2.73 ± 0.23 μW. Note that this value of threshold represents the upper bound, without accounting for multiple reflection/absorptions inside the disk and coupling efficiency of the pump beam. This ultralow threshold promises minimal
power consumption for subwavelength lasers. A logarithmic plot of the L-L curve in the inset image exhibits the “S-shaped” nonlinear transition from spontaneous emission to lasing.

Statistical analysis based on a random sampling (ten lasers for each sample) of MDLs on the IoS template and InP substrate was conducted for a fair comparison of the lasing thresholds, considering the variations in the microdisk fabrication processes. 4-μm-diameter disks were fabricated simultaneously as references to investigate the dependence of device performance when the disk size shrinks to subwavelength dimensions. The statistical lasing thresholds as a function of wavelength for the 1.5-μm and 4-μm disks are shown in Figs. 3(a) and 3(b), respectively. In both cases, the average thresholds for the MDLs on silicon are about twice the average values of those on InP substrates. The larger thresholds on silicon are mainly associated with three factors. First, the material gain in the QD active region on silicon is somewhat lower due to non-radiative recombination processes introduced by crystalline defects. Second, the injection efficiency is reduced because the pump laser generated carriers can be partially trapped by the deep energy level traps related with dislocations in the disk region. Moreover, the broader PL emission spectrum of the MDL on silicon is more favorable for multi-mode lasers (Fig. 1(f)). It is noted in Fig. 3(a) that single-mode lasers (solid circles) among the measured devices on silicon generally exhibit lower thresholds than their multi-mode counterparts (open circles) due to mode competition. Figure 3(b) clearly shows that as the disk diameter increases, the thresholds become larger for MDLs on both substrates. Understandably, smaller active volume in a subwavelength MDL requires less pump power for lasing, leading to a lower threshold. Conversely, the free spectral range (FSR) increases as the disk size diminishes (FSR = k^2/2πn_eff ≈ 142 nm for d = 1.5 μm, and ~53 nm for d = 4 μm). Smaller mode separation for larger disks is more inclined to show multi-mode lasing (open symbols in Fig. 3(b)). Here, only the lowest radial order modes with different azimuthal mode numbers (TE_{1,6} and TE_{1,7} modes in Fig. 3(c) denote radial mode number l = 1, and azimuthal mode number m = 6 or 7) appear in the 1.5 μm MDLs because higher radial order modes may easily be coupled into the supporting post (undercut ~270 nm). This is further verified by numerical simulation of the exact device topology using finite-difference time-domain (FDTD) method. The inset of Fig. 3(c) demonstrates a three-dimensional view of the electric field distribution of the TE_{1,6} WGM, illustrating most of the electric field confined around the periphery of the disk and the evanescent field that introduces radiative losses. Figure 3(c) overlays one of the typical multi-mode lasing spectra with their threshold powers of four MDLs. Matching the
peak wavelengths with the simulation results clearly reveals that the two lasing modes are in the first radial order with adjacent azimuthal mode numbers, and the separation is equal to a FSR. Dispersion in the peak wavelength is a result of disk diameter fluctuation in fabrication.

In addition to the extremely low thresholds ($P_{\text{th}}$), it is of great importance for lasers to operate at chip temperatures. Figures 4(a) and 4(b) display a set of L-L curves at various substrate temperatures ranging from 20°C to 60°C, for the TE$_{1,7}$ and TE$_{1,6}$ modes respectively. No heat sinks or heat dissipations were applied to the lasers. It should be pointed out that the lasers remain lasing at an elevated chip temperature above 60°C, limited by the measurement setup. Compared with the TE$_{1,7}$ mode on the higher energy side (1406.2 nm), the slope efficiency of the TE$_{1,6}$ mode (1546.9 nm) is larger with a higher output intensity under the same pumping condition, suggesting (1) higher capture efficiency in larger QDs, and (2) reabsorption of higher energy photons before coupling to cavity modes. Figures 4(c) and 4(d) highlight the lasing peak wavelengths and thresholds of these two modes as a function of substrate temperature, respectively. First, the lasing wavelengths redshift by 0.07–0.09 nm/°C in the temperature range of 20–60°C for both modes, resulting from the InAs bandgap shrinkage and temperature-induced cavity effective refractive index change. The different wavelength variations with temperature for the two modes can be explained by different effective refractive index change coefficients ($\Delta n_e \propto T$) as a function of temperature. Inset of Fig. 4(c) presents the zoomed-in emission spectra as the substrate temperature was ramped up with the pump power doubling the lasing thresholds, offset vertically for clarity. The intensity ratio of TE$_{1,6}$ to TE$_{1,7}$ becomes larger at a higher temperature ($P_{\text{pump}} = 2 \times P_{\text{th}}$), because of the temperature redshift in the material gain. A high characteristic temperature, $T_0 = 123$ K in the range of 20–60°C was extracted for the TE$_{1,6}$ mode in Fig. 4(d), which is among the best reported $T_0$ for QD MDLs on III-V substrates. The temperature-insensitive characteristics demonstrated here can potentially promote their applications in silicon photonics. Here, the larger $T_0$ of the TE$_{1,6}$ mode can be attributed to a better overlap with the gain spectrum at higher temperatures and a superior carrier capture efficiency in larger QDs, which prevent carrier evaporation into barriers.

In summary, we have demonstrated 1550 nm subwavelength quantum dot microdisk lasers monolithically grown on (001)-Si substrates. Room-temperature pulsed lasing has been achieved with an extremely low threshold of 2.73 μW by combining the small microdisk cavity and quantum dots. These ultra-small lasers can operate at an elevated temperature beyond 60°C, with a large characteristic temperature of 123 K. This approach therefore indicates the feasibility of directly grown on-chip light sources for Si photonics and holds promise for low-cost, large-scale, and high-density photonic integrated circuits. Continuous effort is being made on further improved InP/Si buffer and more uniform QDs with a higher dot density, which will produce high-efficiency electrically injected p-doped QD lasers.

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